

Type	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	IS&R 5	(("6200821") or ("6291292") or ("6355952") or ("6407422") or ("6603161")).PN.	USPAT; US-PGPUB	2004/06/18 16:46		0	
2	BRS 9149	438/\$.ccls. and (random adj access adj memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 16:47		0	
3	BRS 987	(438/\$.ccls. and (random adj access adj memory)) and (ferroelectric)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 16:48		0	
4	BRS 418	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (ferroelectric adj capacitor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 16:49		0	
5	BRS 145	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 18:06		0	
6	BRS 397	438/3 and (diffusion adj barrier)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 11:58		0	
7	BRS 212	(438/3 and (diffusion adj barrier)) and (silicon adj nitride)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/18 18:07		0	
8	BRS 397	438/3 and (diffusion adj barrier)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 11:58		0	

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9	BRS 0	((438/3 and (diffusion adj barrier)) and (merged adj top adj electrode))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 12:00		0	
10	BRS 9149	438/\$.ccls. and (random adj access adj memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 12:02		0	
11	BRS 418	(438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 12:03		0	
12	BRS 145	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 13:11		0	
13	BRS 145	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 13:16		0	
14	BRS 145	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 13:17		0	
15	BRS 0	((438/\$.ccls. and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier) and (merged top adj electrodeplate line adj capacitor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 13:26		0	
16	BRS 1055	257/295 and (random adj access adj memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 13:23		0	

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17	BRS 442	((257/295 and (random adj access adj memory)) and (ferroelectric adj capacitor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 13:24			0
18	BRS 133	((257/295 and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 13:27			0
19	BRS 0	(((257/295 and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)) and (merged adj electrode)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 13:25			0
20	BRS 133	((257/295 and (random adj access adj memory)) and (ferroelectric adj capacitor)) and (diffusion adj barrier)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/21 13:27			0